

**Amendments to the Claims:**

1. (currently amended) A method for forming a buried plate in a trench capacitor, the method comprising the steps of:
  - forming at least one trench with a sidewall in a semiconductor substrate;
  - partially filling the trench with a dopant source material to form a dopant source having a top surface below a top of the trench, the dopant source material containing at least one dopant;
  - ~~covering~~ forming a dielectric collar on the sidewall of the trench above the dopant source with a second material;
  - heating the substrate to cause the dopant to diffuse into the substrate in the trench not covered by the second material ~~dielectric collar~~, thereby forming the buried plate; and
  - removing the dopant source material from the trench.
2. (original) The method of Claim 1, wherein the semiconductor substrate is formed of silicon.
3. (original) The method of Claim 1, wherein the trench is partially filled by a method comprising the steps of:
  - filling the trench with a dopant source material to form a dopant source having a top surface at or above the top of the trench; and
  - recessing the top surface of the dopant source below the top of the trench.
4. (original) The method of Claim 1, wherein the dopant source material is arsenic-doped glass.
5. (currently amended) The method of Claim 29 4, wherein the dielectric collar is formed of nitride.

10/604,081

FIS920030155US1

~ 2 ~

6. (original) The method of Claim 5, wherein the dielectric collar is formed by low pressure chemical vapor deposition.
7. (original) The method of Claim 5, further comprising the step of forming a thin layer of oxide on the sidewall of the trench prior to forming the dielectric collar.
8. (original) The method of Claim 1, wherein the substrate is heated to a temperature of about 800 C to about 1200 C, for a time of about 1 to about 60 minutes.
9. (original) The method of Claim 8, wherein the substrate is heated to a temperature of about 1050 C.
10. (original) The method of Claim 1, wherein the substrate is heated in an oxygen-containing atmosphere.
11. (original) The method of Claim 10, wherein during the heating step, a layer of oxide is grown between the dopant source material and the substrate.
12. (original) The method of Claim 11, further comprising the step of removing the layer of oxide, thereby forming a bottle-shaped trench.
13. (original) The method of Claim 1, further comprising the step of depositing a plurality of hemispherical grains in the trench after the dopant source material is removed.
14. (currently amended) The method of Claim 1, further comprising the step of removing the second material ~~dielectric collar~~.

10/604,081

FIS920030155US1

~ 3 ~

15. (original) The method of Claim 1, further comprising the step of etching the substrate in the lower portion for form a bottle shape trench after the dopant source material is removed.

16. (original) The method of Claim 15, wherein the substrate is etched using ammonia.

17. (currently amended) ~~A method for forming a buried plate in a trench capacitor, the method comprising the steps of:~~  
~~forming at least one trench with a sidewall in a semiconductor substrate;~~  
~~partially filling the trench with a dopant source material to form a dopant source having a top surface below a top of the trench, the dopant source material containing at least one dopant;~~

The method of Claim 1, wherein the sidewall of the trench is covered by  
depositing the a second material on the dopant source, thereby filling the remainder of the trench and covering the sidewall of the trench above the dopant source;

~~heating the substrate to cause the dopant to diffuse into the substrate in the trench not covered by the second material, thereby forming the buried plate; and~~  
~~removing the second material and the dopant source material from the trench.~~

18-20. (canceled)

21. (original) The method of Claim 17, wherein the second material is un-doped oxide.

22. (original) The method of Claim 21, wherein the second material is deposited by low pressure chemical vapor deposition or high density plasma chemical vapor deposition.

10/604,081

FIS920030155US1

23-26. (canceled)

27. (original) The method of Claim 1, further comprising the step of exposing the substrate to at least one of gas phase doping, plasma doping and plasma immersion ion implantation.

28. (original) The method of Claim 13, further comprising the step of exposing the substrate to at least one of gas phase doping, plasma doping and plasma immersion ion implantation, after depositing a plurality of hemispherical grains.

29. (new) The method of Claim 1, wherein the sidewall of the trench is covered by forming a dielectric collar on the sidewall above the dopant source.

10/604,081

~ 5 ~

FIS920030155US1